

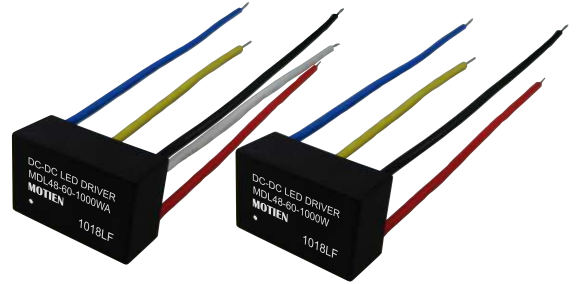
MDL48W(WA) Serise

High Efficiency Step Down LED Driver



Features

- RoHS-compliant 24 Pin DIL Package wired version
- Constant Current Output (±7% Output Current Accuracy)
- LED Driver Current 150 / 250 / 300 / 350 / 500 / 600 / 700 / 1000mA
- Power LED Driver
- Wide Input Voltage Range: 7V to 60V (65V for 0.5sec.)
- Output Power 9 / 14 / 17 / 20 / 29 / 34 / 40 / 48W
- Driver LED Strings of up to 57V (2V to 57V)
- High Efficiency (up to 97%)
- PWM/Digital Dimming and Analog Voltage Dimming
- Open and Short LED Protection
- -40°C ~ 85°C Operation Temperature Range
- With MLCC Capacitors only
- IP67 rated



Application

- 12V,24V,36V and 48V Lighting Systems
- Household/Commercial lighting
- Suitable for high illumination LED
- Power limited (battery) lighting system

MDL48W(WA) Series is a high efficiency step-down converter optimized to drive high current LEDs. The control algorithm allows highly efficient and accurate LED current regulation. The device operates from an input 7Vdc to 60Vdc and provides an externally adjustable output current of up to 1000mA and output power up to 48 watts. Compact size of DIL24 allows designer to integrate this driver together with LED module. UL94V-0 grade molded case with high grade filling material provide excellent fire proof characters.

(Typical at Ta = +25°C, nominal input voltage, rated output current unless otherwise specified.)

Electrical Specifications:	
Input Voltage (Vdc)	7V ~ 60V (65V for 0.5 sec)
Input Filter	Capacitor
Output Voltage Range (Vin = 60V)	2V to 57V
Output Current Range (Vin - Vout > 3V)	See table
Output Current Accuracy	See table
Output Power	See table
Ripple and Noise, (20 MHz bandwidth)	See table
Maximum Efficiency at Full Load	97%
Capacitive Load	470uF
Operating Frequency	20 kHz ~ 500 kHz
Short Circuit Protection	Regulated at Rated Output Current
Temperature Coefficient	±0.03%/°C Max.
Thermal Impedance (Nature Convection)	+30°C/W
Safety Standard : (designed to meet)	IEC / EN 60950-1

Environmental Specifications	
Operating Temperature Range	-40°C to +85°C (See Derating Curve)
Storage Temperature Range	-40°C to +125°C
Environmental	IP67
Maximum Case Temperature	+110°C
Cooling	Nature Convection
Reliability Calculated MTBF (MIL-HDBK-217 F)	>950 Khrs
Soldering Temperature (1.5mm from case 10 sec.)	+260°C

EMC Specifications (design to meet)	
EMI Radiated & Conducted Emissions	EN 55015 (CISPR22)

PWM Dimming and ON/OFF Control (Leave Open if Not Used) For WA	
Remote ON/OFF	
DC/DC ON	Open or 0.3V < V _{ADJ} < 1.25V
DC/DC OFF (Shutdown)	V _{ADJ} < 0.15V
Remote Pin Drive Current (V _{ADJ} = 1.25V)	<1mA
Quiescent Input Current in Shutdown Mode (Vin = 60V)	100uA Max.
PWM Dimming	
Recommended Maximum Operation Frequency	1KHz
Adjust Output Current (PWM Frequency < 300Hz)	0.1% to 100%

Analog Dimming Control (Leave Open if Not Used) For WA	
V _{ADJ} Input Voltage Range	0.3V to 1.25V
Adjust Output Current (Vin - Vout < 30V)	25% to 100%
Control Voltage Range Limits	
ON	0.2V ~ 0.3V
OFF	0.15V ~ 0.25V
Analog Pin Drive Current (V _{ADJ} = 1.25V)	<1mA

Physical Specifications	
Case Material	Non-Conductive Black Plastic (UL94V-0 rated)
Potting Material	Epoxy (UL94V-0 rated) Silicon (UL94V-0 rated)
Lead wires	UL 1015/CSA TEM listed/ 22AWG / 600V/ 105°C Rated
Weight	18.0g
Dimensions	1.25"x0.80"x0.49"

NOTE

1. Reversed power source damages the circuit, No connection is allowed between input ground and output.
2. DO NOT operate the driver over output power.
3. Leave pin V_{ADJ} open if not in use, ground pin to shut down the converter. Connecting V_{ADJ} to Vin damages the circuit.
4. Maximum output open voltage is equal to input voltage.

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PART NUMBER STRUCTURE

MDL48 - 60 - 1000 W A

Series Name

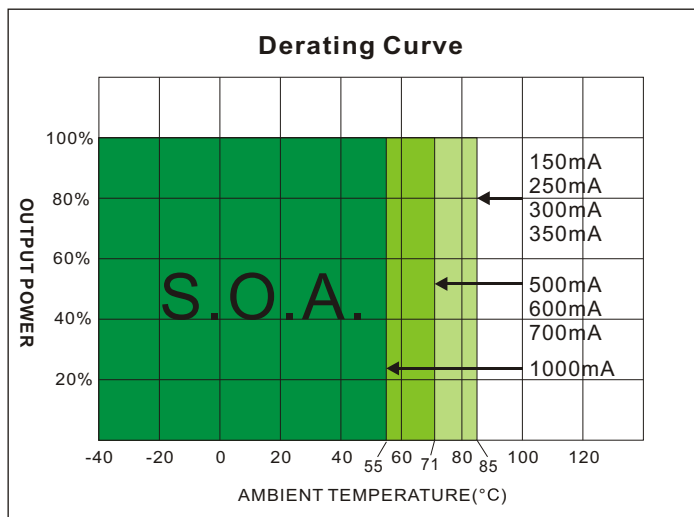
Input Max. Voltage

Wired Version

Output Current

- 150 - 150mA
- 250 - 250mA
- 300 - 300mA
- 350 - 350mA
- 500 - 500mA
- 600 - 600mA
- 700 - 700mA
- 1000 - 1000mA

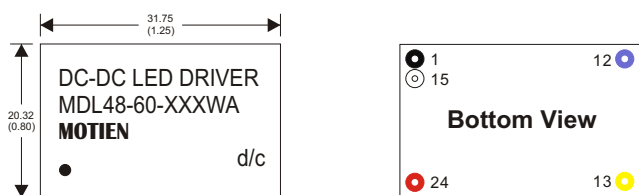
Dimming Control Wire (Optional)



MODEL SELECTION GUIDE

MODEL NUMBER	INPUT	OUTPUT		OUTPUT Current Accuracy (%)	OUTPUT Power (W) Max.	EFFICIENCY @FL(%) Max.	Ripple and Noise mVp-p Max.	Capacitor Load(uF)
	Voltage Range (Vdc)	Voltage Range (Vdc)	Current (mA)					
MDL48-60-150W(WA)	7-60	2 ~57	150	±8	9	97	150	470
MDL48-60-250W(WA)	7-60	2 ~57	250	±7	14	97	200	470
MDL48-60-300W(WA)	7-60	2 ~57	300	±6	17	97	250	470
MDL48-60-350W(WA)	7-60	2 ~57	350	±5	20	97	300	470
MDL48-60-500W(WA)	7-60	2 ~57	500	±5	29	97	400	470
MDL48-60-600W(WA)	7-60	2 ~57	600	±5	34	97	450	470
MDL48-60-700W(WA)	7-60	2 ~57	700	±5	40	97	500	470
MDL48-60-1000W(WA)	7-60	2 ~48	1000	±5	48	97	800	470

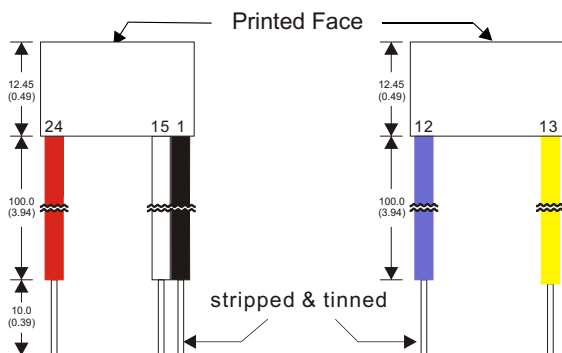
MECHANICAL DIMENSION



24 Pin DIL Package wired version

- Notes : All dimensions are typical in millimeters (inches).
1. Wire core diameter: 0.75±0.05 (0.02±0.002)
 2. Wire outside diameter: 2.4±0.05
 3. Wire length = 100 + 10 stripped & tinned = 110±5 total
 4. Case Tolerance: ±0.5 (±0.02)

Lead wires are under the specification of general lamps :
Wire is UL1015/CSATEM listed #22AWG / 600V / 105°C Rated



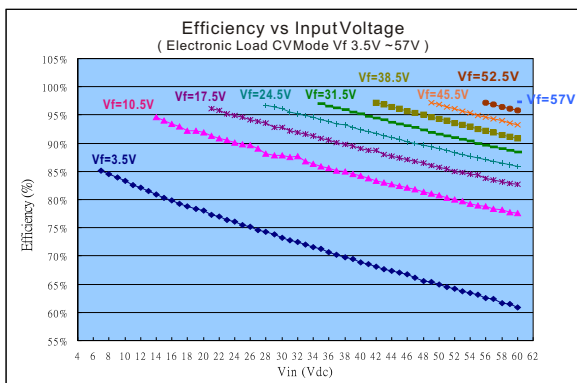
Pin #	CONNECTIONS		
		MDL48-60-XXXWA	MDL48-60-XXXW
1 (Black)	- V Input	- DC Supply	- DC Supply
15 (White)	V _{ADJ}	PWM/ON/OFF or not used	No wires
12 (Blue)	- V Output	LED Cathode Connection	LED Cathode Connection
13 (Yellow)	+V Output	LED Anode Connection	LED Anode Connection
24 (Red)	+V Input	+DC Supply	+DC Supply

No connection is allowed between input and output

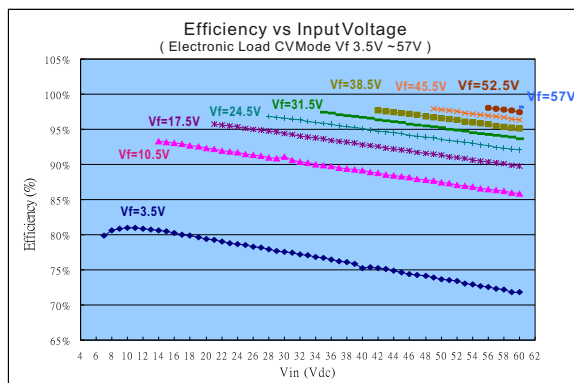
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Typical Operating Conditions

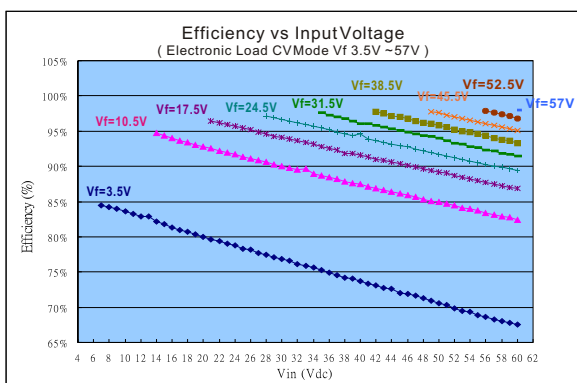
MDL48-60-150WA



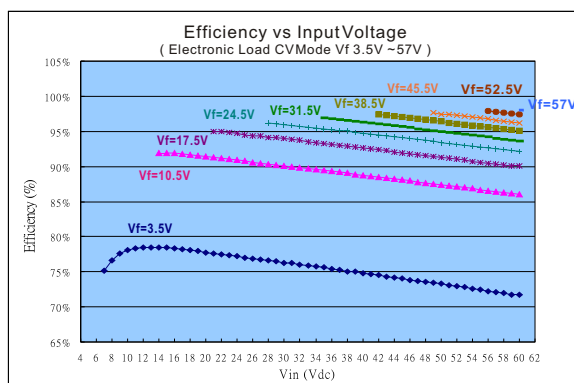
MDL48-60-500WA



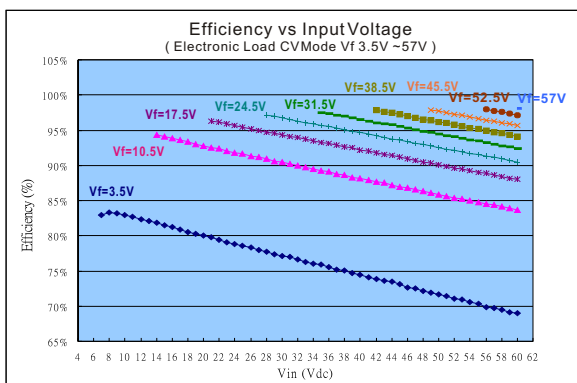
MDL48-60-250WA



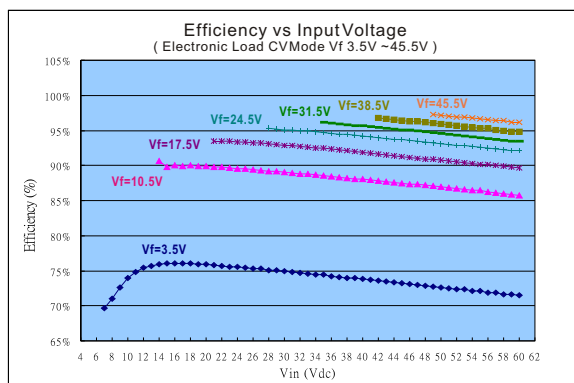
MDL48-60-700WA



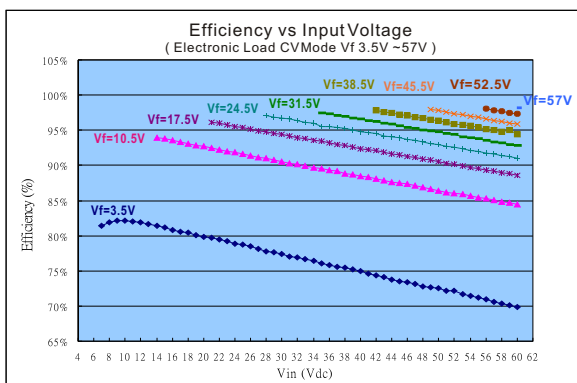
MDL48-60-300WA



MDL48-60-1000WA



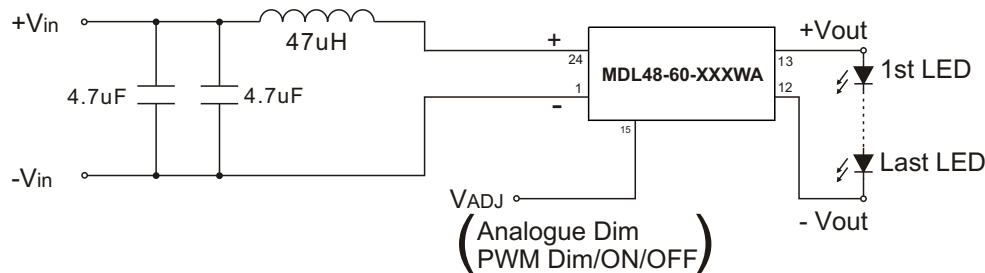
MDL48-60-350WA



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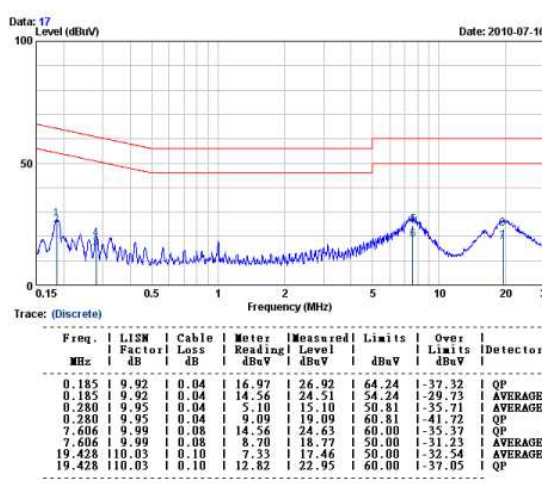
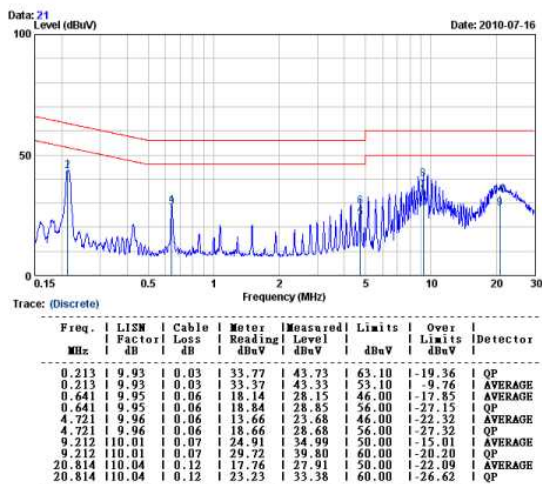
EMC Characteristics meet EN55022

EN55015 Conduction Class B Filter Suggestion



Vin=60V Vout=30V (LED Load Vf=3.3V , 9LED ≐ 30V)

Vin=12V Vout=3.3V (LED Load Vf=3.3V , 1LED ≐ 30V)

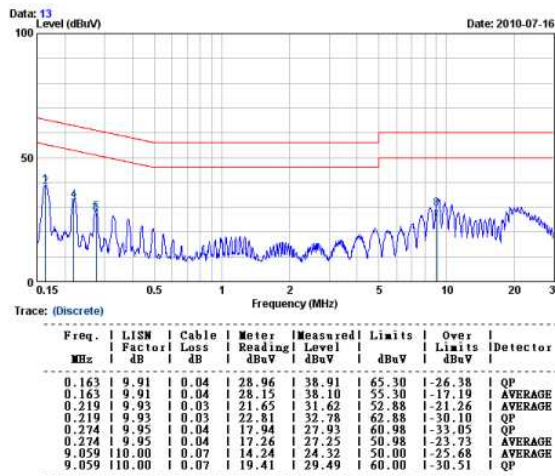
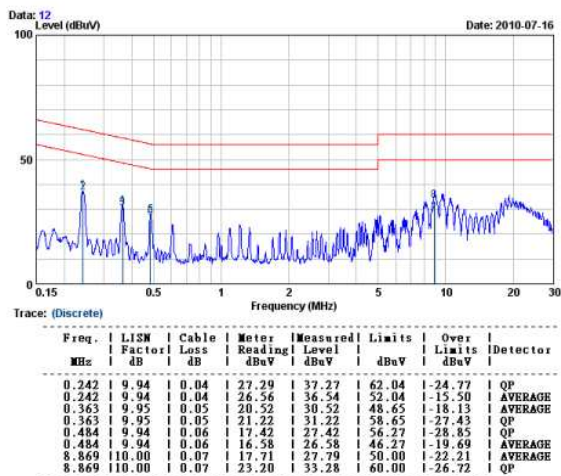


REMARKS: 1.Level(dBuV/m)=Read Level(dBuV)+Antenna Factor(dB/m)+Cable loss(dB)
2.Over Limit value(dB)=Level(dBuV/m)-Limit Line(dBuV/m)

REMARKS: 1.Level(dBuV/m)=Read Level(dBuV)+Antenna Factor(dB/m)+Cable loss(dB)
2.Over Limit value(dB)=Level(dBuV/m)-Limit Line(dBuV/m)

Vin=60V Vout=48V (LED Load Vf=3.3V , 14LED ≐ 15V)

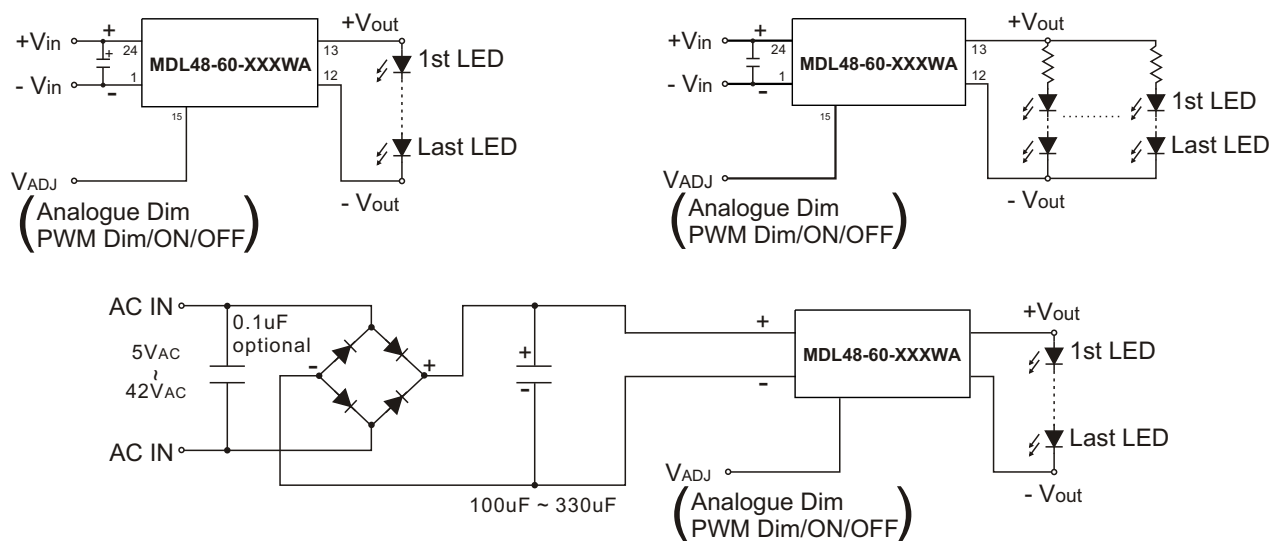
Vin=7V Vout=3.3V (LED Load Vf=3.3V , 1LED ≐ 30V)



REMARKS: 1.Level(dBuV/m)=Read Level(dBuV)+Antenna Factor(dB/m)+Cable loss(dB)
2.Over Limit value(dB)=Level(dBuV/m)-Limit Line(dBuV/m)

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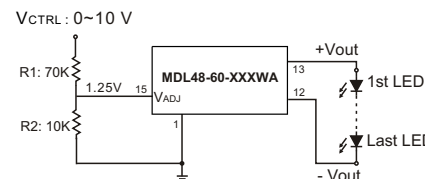
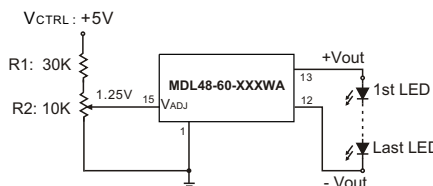
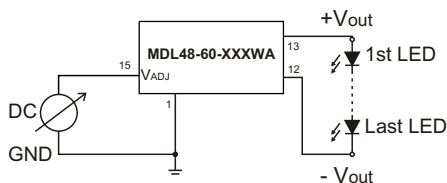
Typical Application



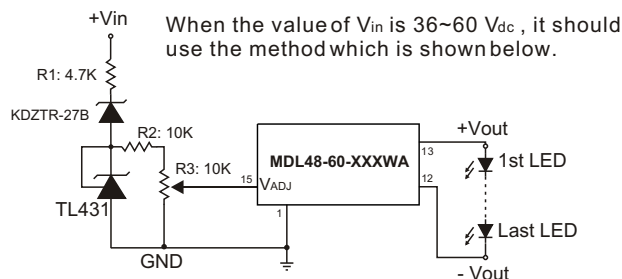
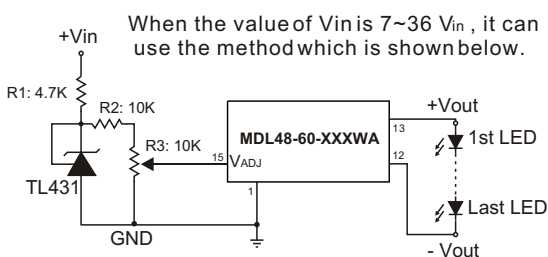
Output Current Adjustment By External DC Control Voltage (V_{CTRL})

$V_{ADJ} = V_{CTRL}$ [If $V_{CTRL} = 0 \sim 1.25V_{dc}$]

$V_{ADJ} = \frac{R2}{R1 + R2} \times V_{CTRL}$ [If $V_{CTRL} > 1.25 V_{dc}$]



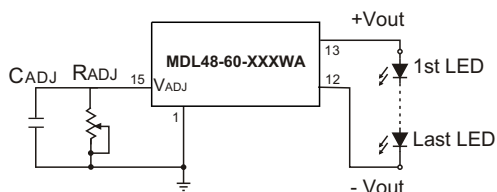
$V_{ADJ} = \frac{R3}{R2 + R3} \times 2.5$ [If $V_{CTRL} = V_{in}$]



The nominal output current (I_{outnom}) is given by: $I_{outnom} \approx I_{out} \times \frac{V_{ADJ}}{1.25}$

Resistor dimming

By connecting a variable resistor between ADJ and GND, simple dimming can be achieved. Capacitor C_{ADJ} is optional for better AC mains interference and HF noise rejection. Recommend value of C_{ADJ} is 0.22µF.



The current output I_{outnom} can be determined using the equation:

$I_{outnom} = \frac{I_{out} \times R_{ADJ}}{(R_{ADJ} + 50K)}$

If the value of R_{ADJ} is 0 to 2M ohm, the maximum adjust range of output current is 25% to 90%. (For $V_{in} - V_{out} < 30V$)

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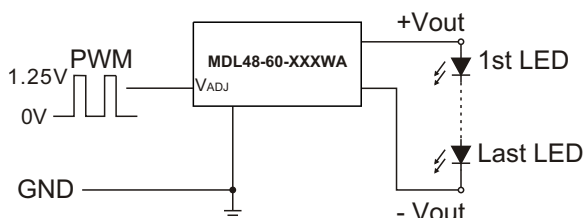
Typical Application

Output Current Adjustment By PWM Control

Directly driving ADJ input

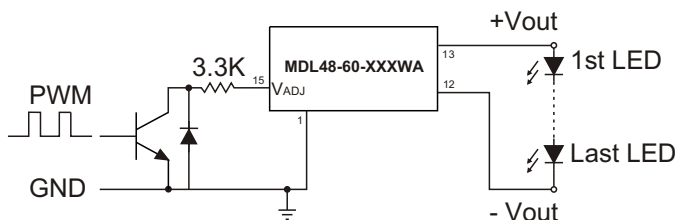
A Pulse Width Modulated (PWM) signal with duty cycle DP_{PWM} can be applied to the ADJ pin, as shown below

$$I_{outnom} \approx I_{out} \times DP_{PWM} \quad [\text{If PWM frequency} < 300\text{Hz, for } 0.001 < DP_{PWM} < 1]$$



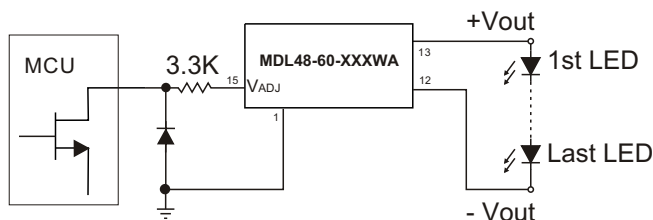
Driving the ADJ input via open collector transistor

The diode and resistor suppress possible high amplitude negative spikes on the ADJ input resulting from the drain-source capacitance of the transistor. Negative spikes at the input to the device should be avoided as they may cause errors in output current, or erratic device operation.



Driving the ADJ input from a microcontroller

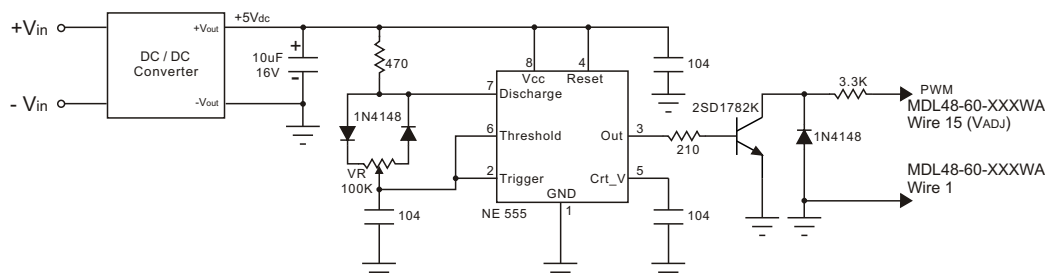
Another possibility is to drive the device from the open drain output of a microcontroller. The diagram below shows one method of doing this:



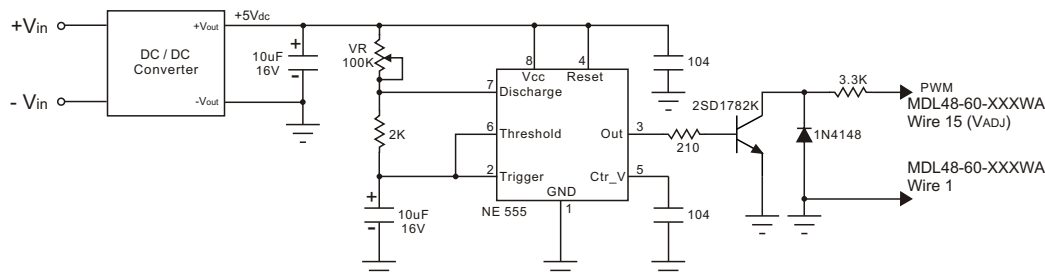
The diode and resistor suppress possible high amplitude negative spikes on the ADJ input resulting from the drain-source capacitance of the FET. Negative spikes at the input to the device should be avoided as they may cause errors in output current, or erratic device operation.

Output Current Adjustment By PWM Control (Dimming)

To avoid visible flicker the PWM signal must be greater than 100Hz.



Output Current Adjustment By PWM Control (Flash)



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